 $\underset{G T I L}{\operatorname{TRAN}} /$ FORMER

## BLOCK DIAGRAM



Balanced Line Out Bypass Note:
When bypass is engaged, we disconnect the internal circuit from the input connector to eliminate any unwanted loading or interaction with the source from the internal circuit. The balanced output is generated by the API 2520 and transformer circuit. With the disconnection of the input, then there is obviously no way to generate the balanced output. Please refer to the block diagram for further details.

| COMPRESSOR (Ratio setting 2:1): |  |  |  |
| :--- | :--- | :--- | :--- |
| Threshold (Int Level) Make-Up Gain Thru-Level  <br> 1 $-15 d B u$ 0 dB -4 dB <br> 2 -21 dBu 2 dB -2 dB <br> 3 -25 dBu 4 dB 0 dB <br> 4 -29 dBu 10 dB +2 dB <br> 5 -34 dBu 14 dB +4 dB <br> 6 -40 dBu 18 dB +6 dB |  |  |  |

COMPRESSOR CURVES (Internal level vs Output level)


